

TSMC-00-559



February 25, 2002

*GP/2838*  
*#4*

To: Commissioner of Patents and Trademarks  
Washington, D.C. 20231

Fr: George O. Saile, Reg. No. 19,572  
20 McIntosh Drive  
Poughkeepsie, N.Y. 12603

RECEIVED  
MAR 12 2002  
TECHNOLOGY CENTER 2838

Subject:

Serial No. 10/050,445 01/16/02

Cheng-Hsiung Kuo

VOLTAGE REGULATOR WITH LOW  
SENSITIVITY TO BODY EFFECT

Grp. Art Unit: 2838

*for different Case*

#### INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation  
In An Application.

The following Patents and/or Publications are submitted to  
comply with the duty of disclosure under CFR 1.97-1.99 and  
37 CFR 1.56. Copies of each document is included herewith.

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
deposited with the United States Postal Service as first class  
mail in an envelope addressed to: Commissioner of Patents and  
Trademarks, Washington, D.C. 20231, on March 7, 2002.

Stephen B. Ackerman, Reg.# 37761

Signature/Date Stephen B Ackerman 3/1/02

U.S. Patent 5,955,874 to Zhou et al., "Supply Voltage-Independent Reference Voltage Circuit," describes a voltage reference circuit comprising intrinsic MOS transistors. The circuit is independent of voltage supply and insensitive to process and temperature variations.

U.S. Patent 5,783,934 to Tran, "CMOS Voltage Regulator with Diode-Connected Transistor Divider Circuit," describes a CMOS voltage regulator.

U.S. Patent 5,097,303 to Taguchi, "On-Chip Voltage Regulator and Semiconductor Memory Device Using the Same," discloses a voltage regulator comprising diode-connected MOS transistors.

U.S. Patent 5,029,282 to Ito, "Voltage Regulator Circuit," discloses a regulator made up of diode connected MOS FETs.

Sincerely,

  
Stephen B. Ackerman,  
Reg. No. 37761

